

MAR 29 2004



09/431477

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kiran Ganesh et al. Examiner: Phallaka Kik  
Serial No.: 09/431477 Group Art Unit: 2825  
Filed: November 1, 1999 Docket No: 884.141US1  
Title: 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR  
VLSI DESIGN  
Assignee: Intel Corporation Customer No: 21186

**PETITION FOR A THREE-MONTH EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

*See  
Only*

In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a three-month extension of time be granted in which to respond to the Non-Final Office Action mailed September 25, 2003, said period of response being extended from December 25, 2003 to March 25, 2004.

Please charge Deposit Account No. 19-0743 in the amount of \$950.00 to cover the required extension fee. Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.

Respectfully Submitted

KIRAN GANESH ET AL.

By their Representatives,

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Date: March 25, 2004 By: Ann M. McCrackin  
Ann M. McCrackin  
Reg. No: 42,858

**CERTIFICATE UNDER 37 CFR § 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 25 day of March 2004. 09/431477 190743 09431477

Name: Ann McCrackin Signature: Ann M. McCrackin 01/FC:1253 950.00 DA